

REMARKS

Claims 12-17 are currently pending. Claims 1-11 are cancelled without prejudice. Claims 12-17 are added. Reconsideration and continued examination are respectfully requested.

Claims 12-17 are identical to originally filed claims 1-6 which were previously cancelled without prejudice. Claim 12 recites, among other limitations, “repeating (b) and (c) until a combination of 2D addresses that satisfies the one bit difference property is found.” In the office action of 1/10/2006, Examiner indicated that Cohn teaches “repeating (b) and (c) until a combination of 2D addresses that satisfies the one bit difference property is found [an address sequencer embodying the principles of the present invention includes a means for generating a sequence of binary words comprising a repeating cycle of 2^k linear (n, k, d) code words. Each code word contains k data bits and n-k check bits, the k data bits occupying the same positions in each word. The invention further includes a means for generating a sequence of binary words comprising words of a Gray code of dimension n-k. An individual Gray code word occurs consecutively 2^k , and distinct Gray codes words occur in the order in which they appear in the Gray code (column 1, lines 43-63); note that 2^k corresponds to 2D].”

Assignee respectfully traverses. In Cohn, “each code word contains k data bits and n-k check bits”. Cohn teaches “generating a sequence of binary words comprising words of a Gray code of dimension n-k.”. “[T]he first 2^k addresses correspond to the words produced by adding the first Gray code word to every linear code word. The second 2^k addresses correspond to the words produced by adding the second Gray code word to each of the words of the linear code. Similarly the third and subsequent groups of 2^k addresses correspond, respectively, to the words produced by adding the third and subsequent Gray code words to each of the 2^k linear code words.” Cohn, col. 4, line 59 – col. 5, line 2.

It is noted that “adding the first Gray code word to every linear code word” does not produce “a combination of $2D/2^k$ addresses that satisfies the one bit difference property”. This is because the linear portion of the n bit word does not satisfy the foregoing.

Accordingly, Assignee respectfully requests allowance for claims 12 and 15, as well as to dependent claims 13, 14, 16, and 17.

Conclusion

Assignee respectfully submits that each of the pending claims are allowable, making the application in a condition for allowance. Examiner is respectfully requested to pass this case to issuance.

The Commissioner is hereby authorized to charge any deficiency in the amount enclosed or any additional fees which may be required under 37 CFR 1.16 or 1.17 to Deposit Account No. 13-0017 in the name of McAndrews, Held & Malloy, Ltd.

RESPECTFULLY SUBMITTED,



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